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# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

7

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10023651	12/21/2001	174	26	2841	CUNES

**\*\*APPLICANTS:** Kaneda Yutaka;

**\*\*CONTINUING DATA VERIFIED:** NO  
IB Patent

**\*\* FOREIGN APPLICATIONS VERIFIED:** YES  
JAPAN 2000-388499 12/21/2001  
IB Patent

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	111426
Verified and Acknowledged Examiners's initials <i>IB Patent</i>		
TITLE : Flexible wiring boards and manufacturing processes thereof		

U S DEPT. OF COMM /PAT & TM-PTO-435L(Rev 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Total Claims		Print Claim for O.G. 17
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
		Application Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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